FPGA CONFIG

U5
GW1NR-LV9QN88P

GND
PIN88_IOT5A_MODE0_1V8
PIN87_IOT6B_MODE1_1V8
PIN86_IOT8A_PWM_1V8
PIN85_IOT8B_1V8
PIN84_IOT10A_1V8
PIN83_IOT10B_1V8
PIN82_IOT11A_1V8
PIN81_IOT11B_1V8
PIN80_IOT12A_1V8
PIN79_IOT12B_1V8
VCCO0_1_2_3V3
PIN77_IOT37A
PIN76_IOT37B
PIN75_IOT38A
PIN74_IOT38B
PIN73_IOT39A
PIN72_IOT39B
PIN71_IOT41A
PIN70_IOT41B

89 EP
88 IOT5A/MODE0
87 IOT6B/MODE1
86 IOT8A
85 IOT8B
84 IOT10A
83 IOT10B
82 IOT11A
81 IOT11B
80 IOT12A
79 IOT12B
78 VCCX/VCCO0
77 IOT37A
76 IOT37B
75 IOT38A
74 IOT38B
73 IOT39A
72 IOT39B
71 IOT41A
70 IOT41B

BLANK

14 IOL22B — PIN14_IOL22B
15 IOL26A — PIN15_IOL26A
16 IOL26B — PIN16_IOL26B
17 IOB2A — FPGA_TX
18 IOB2B — FPGA_RX
19 IOB4A — PIN19_IOB4A
20 IOB4B — PIN20_IOB4B
21 VSS — GND
22 VCC — +1V2
23 VCCO2 — VCCO0_1_2_3V3

69 IOT42A — PIN69_IOT42A
68 IOT42B — PIN68_IOT42B
67 VCCX/VCCO0 — VCCO0_1_2_3V3
66 VCC — +1V2
65 VSS — GND
64 VCCX/VCCO0 — VCCO0_1_2_3V3
63 IOR5A/RPLL_T_in — PIN63_IOR5A
62 IOR11A/MI/D7 — PIN62_IOR11A_MISO
61 IOR11B/MO/D6 — PIN61_IOR11B_MOSI
60 IOR12A/MCS_N/D5 — PIN60_IOR12A_MCS
59 IOR12B/MCLK/D4 — PIN59_IOR12B_MCLK
58 VCCO1 — VCCO0_1_2_3V3
57 IOR13A/FASTRD_N/D3 — PIN57_IOR13A
56 IOR14A/SO/D1 — PIN56_IOR14A
55 IOR14B/SSPI_CS_N/D0 — PIN55_IOR14B
54 IOR15A/DIN/CLKHOLD_N — PIN54_IOR15A
53 IOR15B/DOUT/WE_N — PIN53_IOR15B
52 IOR17A/GCLKT_3 — PIN52_XTAL_IN
51 IOR17B/GCLKC_3 — PIN51_IOR17B
50 IOR22B — PIN50_IOR22B
49 IOR24A — PIN49_IOR24A
48 IOR24B — PIN48_IOR24B
47 IOB43B — PIN47_IOB43B

24 VSS — GND
25 IOB8A — PICO_21
26 IOB8B — PICO_22
27 IOB11A — PICO_15
28 IOB11B — PICO_14
29 IOB13A — PICO_13
30 IOB13B — PICO_12
31 IOB15A — PICO_11
32 IOB15B — PICO_10
33 IOB23A — PICO_09
34 IOB23B — PICO_08
35 IOB29A/GCLKT_4 — PICO_07
36 IOB29B/GCLKC_4 — PICO_06
37 IOB31A — PICO_05
38 IOB31B — PICO_04
39 IOB33A — PICO_03
40 IOB33B — PICO_02
41 IOB41A — PICO_01
42 IOB41B — PICO_00
43 VSS — GND
44 VCCO2 — VCCO0_1_2_3V3
45 VCC — +1V2
46 VSS — GND

PIN87_IOT6B_MODE1_1V8 — 4.7k — R17 — GND
PIN88_IOT5A_MODE0_1V8 — 4.7k — R19 — GND
IOL13B_RECONFIG_N — 4.7k — R18 — VCCO3_1V8

XTAL

GND
PIN52_XTAL_IN

BLANK

+3V3

CAP-FPGA

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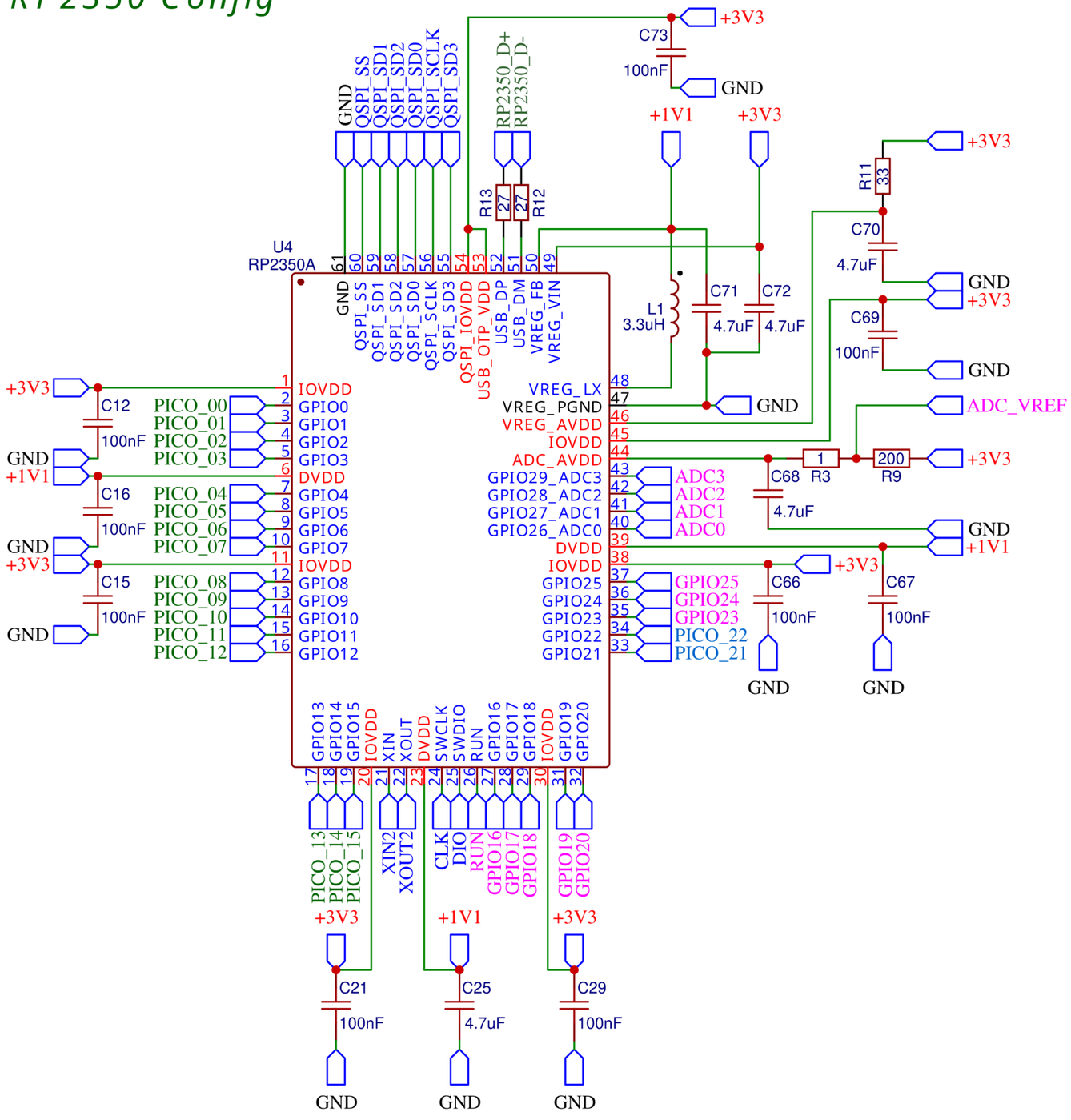
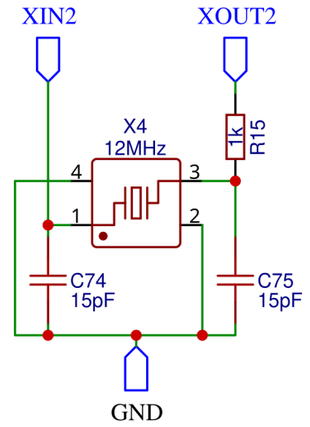
	Company: BrisbaneSilicon	Sheet:
	Date: 15/06/2026 Drawn By: BRS	

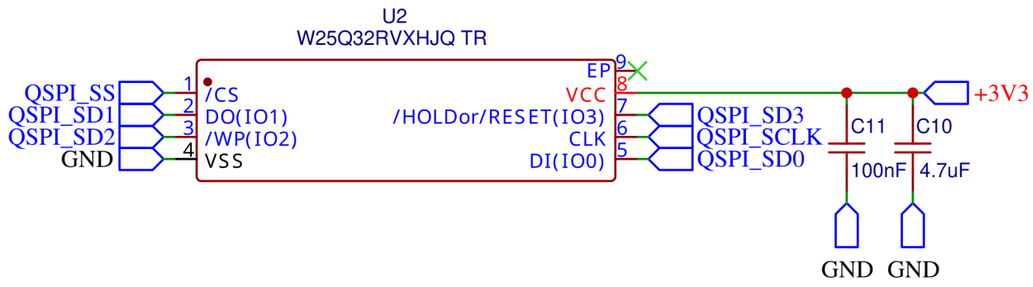
RP2350 Config



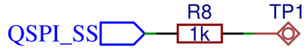
XTAL



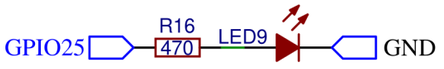
FLASH



BOOT



USER LED



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	Date: 15/06/2026 Drawn By: BRS	

BL702 Config

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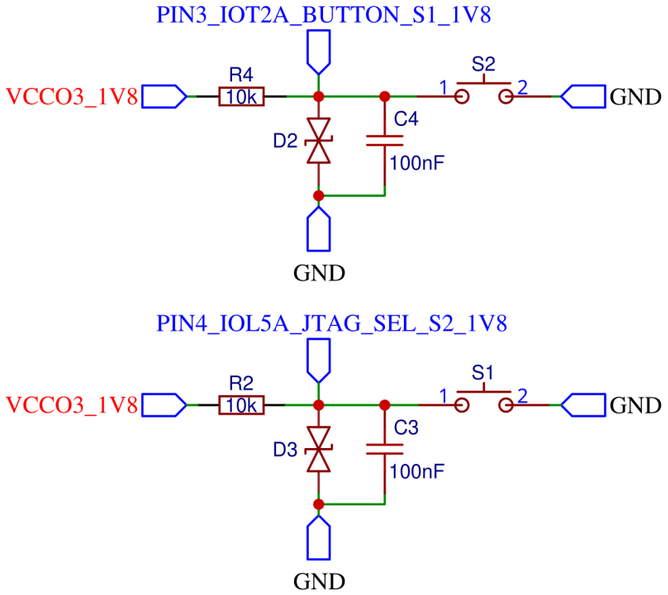
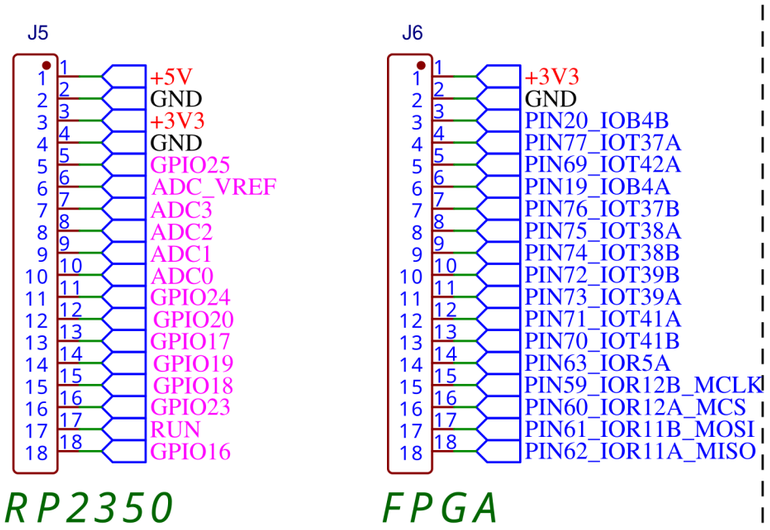
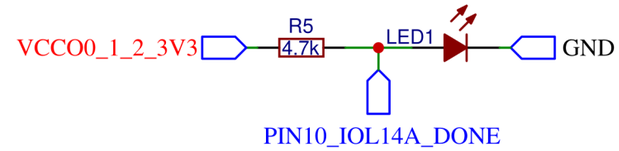
BUTTON x 2



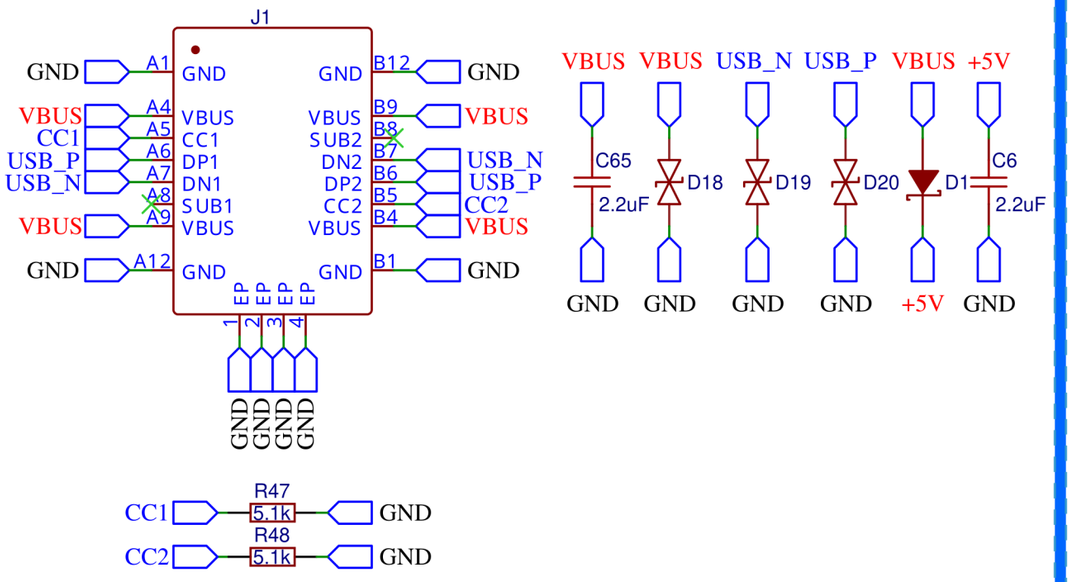
CONNECTOR



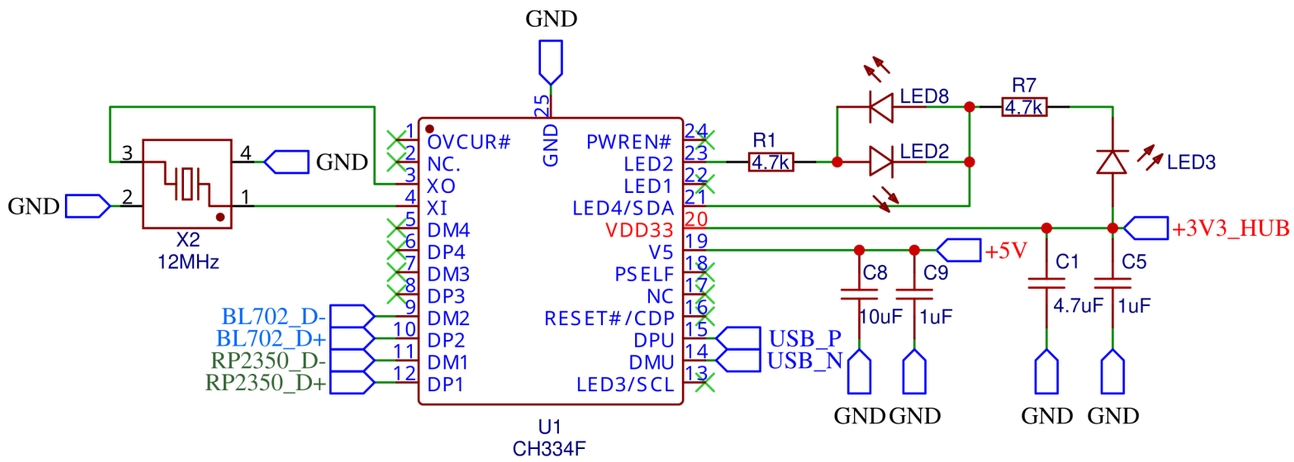
FPGA DONE LED



USB C Config



USB HUB Config



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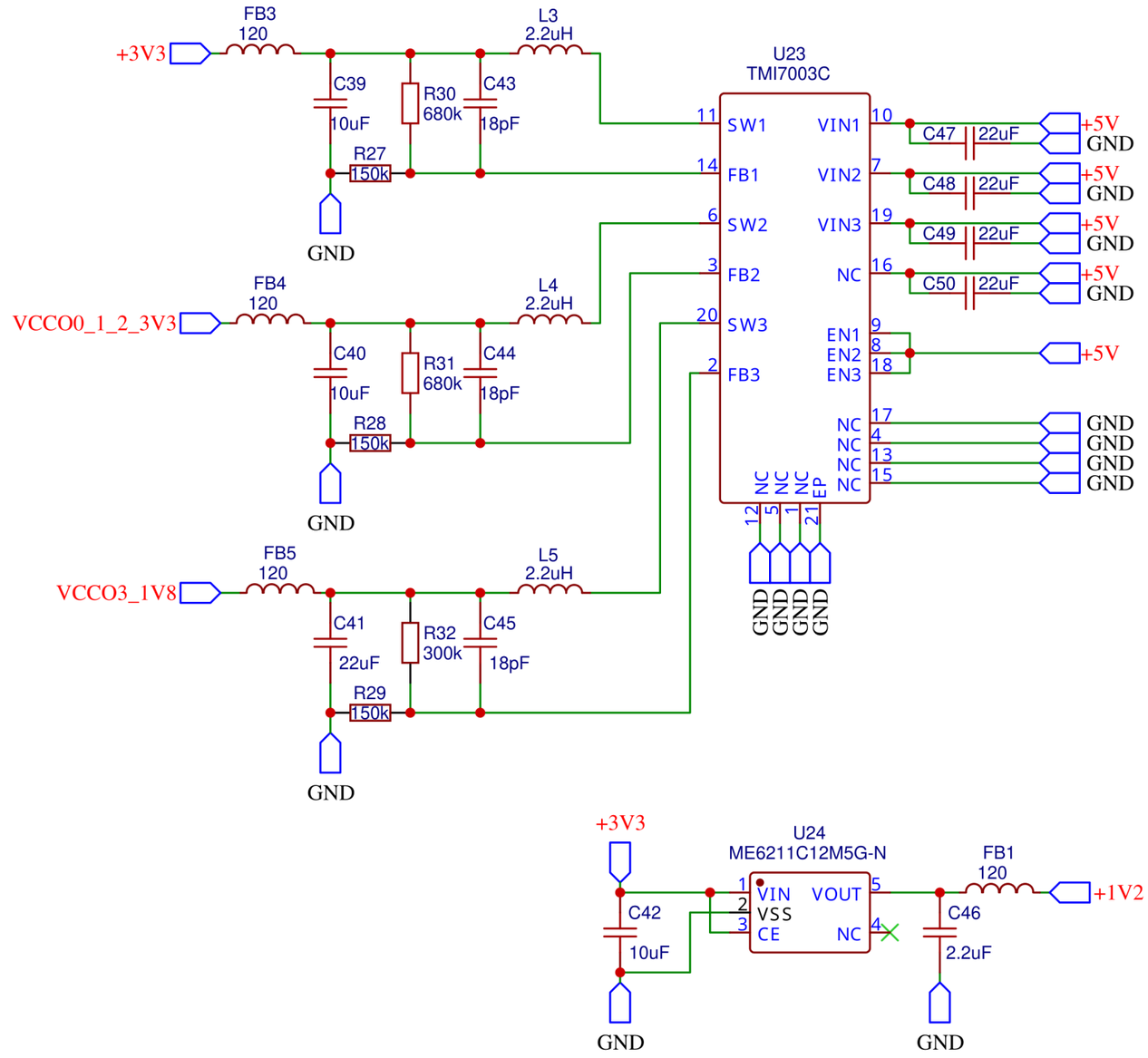
POWER MANAGEMENT



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